

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 114596-28-0053BS		SERIAL NO. 09/626,325		RECEIVED	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates Jr., et al.				AUG 03 2004	
				FILING DATE July 26, 2000		GROUP ART UNIT 2183		Technology Center 2100	
U.S. PATENT DOCUMENTS									
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE			
RE	4,412,303	10/25/1983	Barnes	712	16	Oct. 25, 1983			
RE	4,636,940	01/1987	Goodwin	717	128	Dec. 20, 1985			
RE	4,750,112	06/07/1988	Jones	364	200	Feb. 10, 1984			
RE	5,127,092	06/30/1992	Gupta	712	234	Jun. 15, 1989			
RE	5,168,557	12/01/1992	Shibuya	712	207	Sep. 21, 1989			
RE	5,241,638	08/31/1993	Mork	711	207	Aug. 12, 1995			
RE	5,339,422	8/16/1994	Brender	714	4	Mar. 7, 1991			
	5,355,487		Keller						
RE	5,404,476	04/04/1995	Kadaira	711	207	Mar. 9, 1993			
RE	5,479,616	12/26/1995	Garibay	712	212	Apr. 3, 1992			
RE	5,483,647	01/09/1996	Yu	713	100	Dec. 17, 1992			
RE	5,487,156	01/23/1996	Popsescu	712	217	Dec. 5, 1990			
RE	5,507,028	04/09/1996	Liu	395	375	Jan. 9, 1995			
RE	5,560,013	09/24/1996	Scalzi	717	138	Dec. 6, 1994			
RE	5,574,929	11/12/1996	Pieterse	712	30	Jun. 30, 1994			
RE	5,590,309	12/31/1996	Chencinski	395	472	Apr. 1, 1994			
RE	5,613,118	03/18/1997	Heisch	395	709	Jun. 20, 1994			
RE	5,657,474	08/12/1997	Taine	711	163	Nov. 20, 1995			
RE	5,659,782	08/19/1997	Senter	712	23	Sep. 16, 1994			
RE	5,673,407	09/30/1997	Poland	395	375	Mar. 8, 1994			
RE	5,732,238	03/24/1998	Sarkozy	711	135	Jun. 12, 1996			
RE	5,742,780	04/21/1988	Caulk	395	382	Jan. 16, 1997			
RE	5,764,947	06/09/1998	Murphy	703	20	Mar. 2, 1994			
RE	5,768,500	6/16/1998	Agrawal	714	47	Jun. 20, 1994			
RE	5,781,758	07/14/1998	Morley	395	500	Mar. 23, 1995			
RE	5,802,337	09/01/1998	Fielden	712	216	Dec. 29, 1995			
RE	5,805,877	09/08/1998	Black	395	586	Sep. 23, 1996			
RE	5,812,864	09/22/1998	McCoy	703	26	Dec. 6, 1995			
RE	5,815,720	9/29/1998	Buzbee	717	158	Mar. 15, 1996			
RE	5,819,064	10/06/1998	Razdan	703	27	Nov. 8, 1995			
RE	5,854,913	12/29/1998	Goetz	712	210	Jun. 10, 1997			
RE	5,864,697	01/26/1999	Shiell	712	240	Jun. 27, 1997			
EXAMINER Richard Ellis			DATE CONSIDERED 10/19/2004						

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JUL 28 2004 PATENT & TRADEMARK OFFICE		APPLICANT John S. Yates Jr., et al.		GROUP ART UNIT 2183		
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RE	5,870,763	02/09/1999	Lomet	707	202	Mar. 10, 1997
RE	5,903,750	05/11/1999	Yeh	712	236	Nov. 20, 1996
RE	5,903,751	05/11/1999	Hoyt	395	585	Sep. 16, 1997
RE	5,911,073	06/08/1999	Mattson	395	701	Dec. 23, 1997
RE	5,941,827	02/1996	Holtey	711	163	Jan. 14, 1994
RE	5,960,198	Sep. 28, 1999	Roediger	717	130	Mar. 19, 1997
RE	5,968,162	10/19/1999	Yard	712	203	Apr. 2, 1996
RE	5,983,335	11/09/1999	Dwyer	712	23	Apr. 30, 1997
RE	6,002,872	12/14/1999	Alexander	717	127	Mar. 31, 1998
RE	6,006,033	12/21/1999	Heisch	717	158	Aug. 15, 1994
RE	6,006,277	12/21/1999	Talati	709	313	Jan. 26, 1996
RE	6,021,484	02/01/2000	Park	712	41	Nov. 14, 1997
RE	6,029,222	02/22/2000	Kamiya	710	262	Jan. 10, 1997
RE	6,044,220	03/28/2000	Breternitz	717	139	Feb. 25, 1997
RE	6,047,363	04/2000	Lewchuk	711	213	Oct. 14, 1997
RE	6,052,771	4/18/2000	Heller	712	34	Jan. 20, 1998
RE	6,076,144	6/13/2000	Peled	711	125	Dec. 1, 1997
RE	6,092,188	07/18/2000	Corwin	712	239	Jul. 7, 1999
RE	6,115,809	09/05/2000	Mattson	712	239	Apr. 30, 1998
RE	6,119,204	9/12/2000	Chang	711	141	Jun. 30, 1998
RE	6,141,683	10/31/2000	Kraml	709	220	Jan. 30, 1998
RE	6,195,748	02/27/2001	Chrysos	712	227	Nov. 26, 1997
RE	6,202,205	03/13/2001	Saboff	717	9	Jul. 21, 1998
RE	6,205,545	03/20/2001	Shah	712	237	Apr. 30, 1998
RE	6,212,493	04/03/2001	Huggins	703	22	Dec. 1, 1998
RE	6,233,678	05/15/2001	Bala	712	240	Nov. 5, 1998
RE	6,237,074	05/22/2001	Phillips	711	213	May 26, 1995
RE	6,243,804	06/05/2001	Cheng	712	228	Jul. 22, 1998
RE	6,256,728	07/03/2001	Witt	712	236	Jul. 6, 1998
RE	6,260,191	07/10/2001	Santhanam	717	9	Apr. 7, 1997
RE	6,295,644	09/25/2001	Hsu	717	9	Aug. 17, 1999
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U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE		
RLE	6,298,477	10/02/2001	Kessler	717	145	Oct. 30, 1998		
RLE	6,304,948	10/16/2001	Motoyama	711	162	Oct. 6, 1998		
RLE	6,308,318	10/23/2001	Krishnaswamy	717	139	Oct. 7, 1998		
RLE	6,308,321	10/23/2001	Schooler	717	132	Dec. 11, 1998		
RLE	6,336,135	01/01/2002	Niblett	709	215	Nov. 19, 1998		
RLE	6,351,844	2/26/2002	Bala	717	128	Nov. 5, 1998		
RLE	6,353,924	03/05/2002	Ayers	717	128	Feb. 8, 1999		
RLE	6,363,336	03/26/2002	Banning	703	26	Oct. 13, 1999		
RLE	6,374,367	03/26/2002	Dean	714	37	Nov. 26, 1997		
RLE	6,381,628	04/30/2002	Hunt	709	201	Nov. 20, 1998		
RLE	6,381,735	04/30/2002	Hunt	717	158	Nov. 20, 1998		
RLE	6,453,292	09/17/2002	Ramaswamy	704	235	Oct. 28, 1998		
RLE	6,470,442	10/22/2002	Arimilli	712	32	Jul. 30, 1999		
RLE	6,473,846	10/29/2002	Melchior	711	170	Jun. 16, 2000		
RLE	6,496,923	12/17/2002	Gruner	712	213	Dec. 17, 1999		
RLE	6,549,930	04/15/2003	Chrysos	709	104	Nov. 26, 1997		
RLE	6,557,094	04/29/2003	Pechanek	712	209	Sep. 28, 2001		
RLE	6,560,693	05/06/2003	Puzak	712	207	Dec. 10, 1999		
RLE	6,571,331	05/27/2003	Henry	712	239	Apr. 3, 2001		
RLE	6,591,414	07/08/2003	Hibi	717	151	Oct. 3, 1997		
RLE	6,631,518	10/07/2003	Bortnikov	717	158	Mar. 19, 1997		
RLE	6,678,820	01/2004	Kahle	712	239	Mar. 30, 2000		
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X								
FOREIGN PATENT DOCUMENTS								
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						YES	NO	
X								
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RE		Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264 (1999) www.alphalinux.org/archives/axp-list/June1999/0475.html				
RE		Dean, ProfileMe: Hardware Support for Instruction-Level Profiling on Out-of-Order Processors," IEEE 1997, pp. 292-302				
RE		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm				
RE		Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)				
RE		Reiner W. Hartenstein, Jürgen Becker: Performance Analysis in CoDe-X Partitioning for Structural Programmable Accelerators; Proc. of 5th Int'l Workshop on Hardware/Software Co-Design CODES/CASHE '97, Braunschweig, Germany, p. 141 (March 1997)				
RE		Hollingsworth, Critical Path Profiling of Message Passing and Shared-Memory Programs, IEEE Transactions on Parallel and Distributed Systems vol. 9, no. 10, pp. 1029-1040 (October 1998)				
RE		Intel Corporation, Intel Processor Family Developer's Manual (1997), pages 1-1 to 1-6, 2-1 to 2-20				
RE		Intel Corporation, Intel Processor Family Developer's Manual (1997), vol. 3 (Architecture and Programming Manual), pages 3-1 to 3-3, 3-10 to 3-13, 12-1 to 12-27, 14-1 to 14-30				
RE		Jones, Puzzling with Microcode, ACM SIGARCH Computer Architecture News, vol. 11, no. 5, pp. 8-12 (1983)				
RE		Kavi et al., A Performability Model for Soft Real-Time Systems, IEEE Proceedings of the 27th Annual Hawaii International Conference on System Sciences, pp. 571-579 (1994)				
RE		Kim and Tyson: Analyzing the Working Set Characteristics of Branch Execution, Proceedings of the 31st Annual ACM/IEEE International Symposium on Microarchitecture, pp. 49-58 (Dec. 1998)				
RE		Larus and Schnarr: EEL: Machine-Independent Executable Editing, EEL: Machine-independent executable editing. In Proceedings of the ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI), 1995				
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RE		Magnusson and Werner, Efficient Memory Simulation in SimICS, Proceedings of the 28th Annual Simulation Symposium, IEEE, pp. 62-73 (1995).				
RE		Mueller, Rustagi and Baker: MiThOS - A Real-Time Micro-Kernel Threads Operating System, IEEE Real-Time Systems Symposium 1995: pp. 49-55 (1995)				
RE		Park et al., Evaluation of Scheduling Techniques on a SPARC-Based VLIW Testbed, Proceedings of the 30th annual ACM/IEEE international symposium on Microarchitecture, pp. 104-113 (1997)				
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